

Correction d'EMD up et AP
72 ELM

Questions de cours (7pts)

réponses voir le cours

Exo 1 (4pts)

I/ L'allocation mémoire

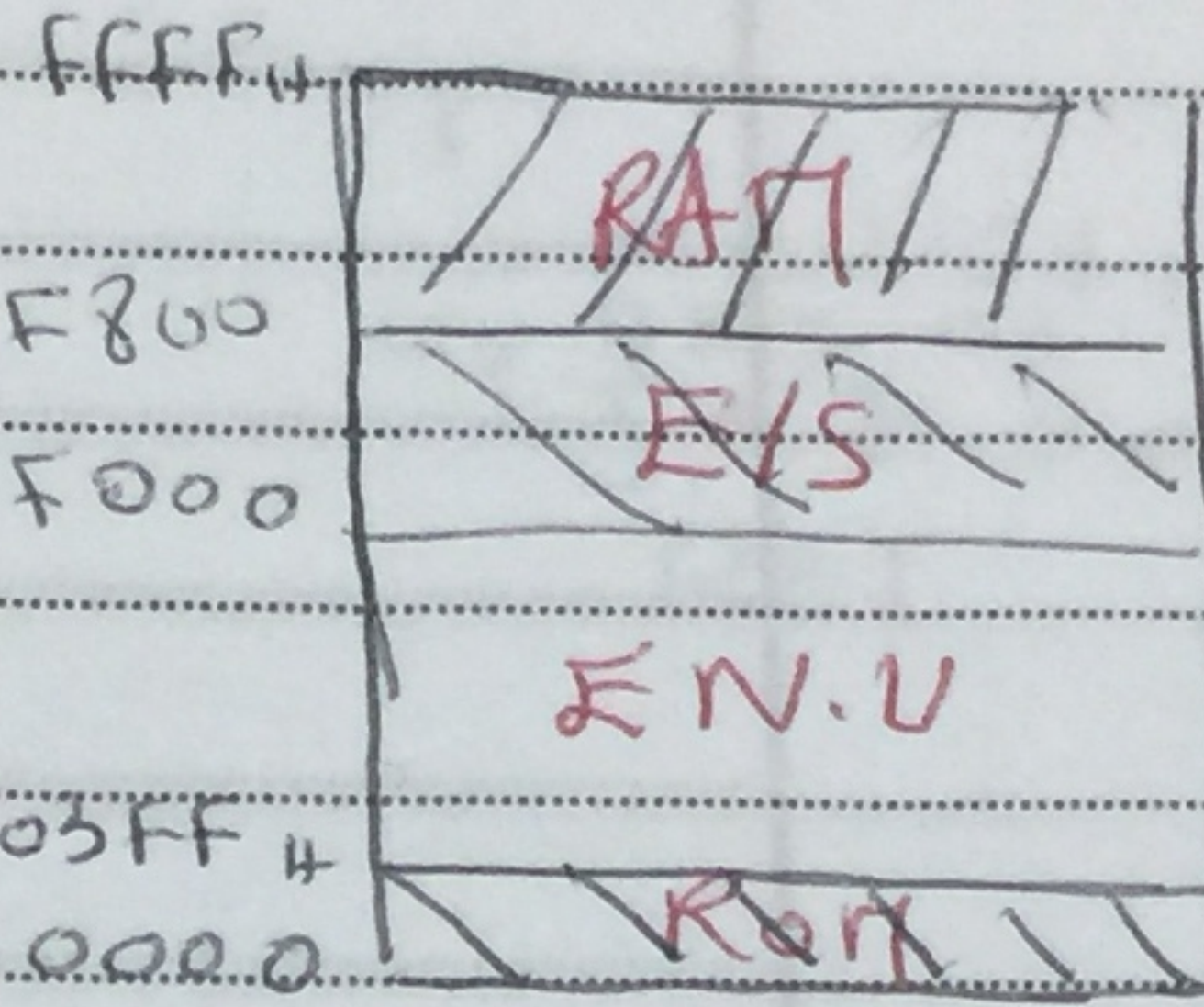
$(A_1 - A_2) \Rightarrow 2^{16} = 64 \text{kd}$

$0000 : FFFF_{16}$

1) ROM : $0300 : 03FF$

4 pages 256×4 (0,5pts)

$256 \times 4 = 1024 = 1 \text{kd}$



(0,5pts)

2) E/S : $F000 : F800 \Rightarrow 8 \text{ page de } 256$

$256 \times 8 = 2048 \Rightarrow 2 \text{kd}$

3) RAM : $F800 : FFFF \Rightarrow 8 \text{ page } 256$

$256 \times 8 = 2 \text{kd} = 2048$

II/ Déterminer le nbu de boitiers ROM (256x8)

et RAM (512x8)

ROM : $1024 = 1 \text{kd} / 256 = 4 \text{ boitiers (0,5pts)}$

RAM : $2048 = 2 \text{kd} / 512 = 4 \text{ " (0,5pts)}$

b/ schéma :

$1024 = 2^{10} \quad | \quad 256 = 2^8$

$2048 = 2^{11} \quad | \quad 512 = 2^9$

(0,5) ROM \Rightarrow parmi 10 ligne 8 entrantes et 2 par \overline{CS}

(0,5) RAM \Rightarrow 4 " 9 " " 2 " \overline{CS}

Suite correction

Ex 02: (6 pts)

```

mov Bx, 0100h
mov DS, Bx
mov AL, [4000h]
mov BL, [4001h]
IMUL BL
js negatif
jz nul
mov AL, 01h
mov Dx, 199h
out Dx, AL
jmp fin
negatif call tempo
jmp fin
nul: mov AL, 00h
mov Dx, 199
out Dx, AL
fin: HLT
    
```

```

temp
mov cx, FFFF
** DEC cx
jnz **
RET
    
```

Ex 03 (03 pts)

```

mov Bx, 5000h
mov DS, Bx
mov Ax, data1
mov Data2, Ax
mov Ax, data1 + 2
mov data2 + 2, Ax
mov Ax, data1 + 4
mov Data2 + 4, Ax
HLT
    
```

0013	Data1 DW 527Ah, 1234h, B2F4h	0013	527A
0015		0014	52
0017		0015	34
0019		0016	12
		0017	F4
		0018	B2
		0019	7A
		0020	52
		0021	B4
		0022	12
		0023	F4
0019	Data2 DW 0, 0, 0.	0024	B2

Mme Dufli